

<b>09:30</b>	<b>Registration</b>	
<b>10:00</b>	<b>Welcome</b>	<b>Steve Drew, Director, TechNES</b>
<b>10:30</b>	<b>Breakout (Networking Session)</b>	<b>All</b>
<b>12:00</b>	<b>FABulous FPGA</b>	<b>Dirk Koch, Manchester University</b>
<b>13:00</b>	<b>Lunch</b>	
<b>14:00</b>	<b>Introduction to Gowin Semiconductor</b>	<b>Nigel Watts &amp; Mike Furnival Gowin Semiconductor</b>
<b>15:00</b>	<b>Design Engineering</b>	<b>Recruitment, Retention &amp; Neil Dickins IC Resources</b>
<b>15:45</b>	<b>Break</b>	
<b>16:00</b>	<b>Accelerating FPGA verification</b>	<b>Piyush Sukhija, Synopsys</b>
<b>16:45</b>	<b>Close</b>	
<b>17:00</b>	<b>Drinks &amp; Networking</b>	