

09:30	Registration	
10:00	Welcome	Steve Drew, Director, TechNES
10:30	Breakout (Networking Session)	All
12:00	FABulous FPGA	Dirk Koch, Manchester University
13:00	Lunch	
14:00	Introduction to Gowin Semiconductor & WPG	Nigel Watts & Richard Simpson Gowin Semiconductor & WMG
15:00	Design Engineering	Recruitment, Retention & Neil Dickins IC Resources
15:45	Break	
16:00	Talk	Piyush Sukhija, Synopsys
16:45	Close	
17:00	Drinks & Networking	